

XTPL. Powering the microelectronics of tomorrow.

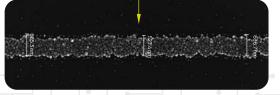
Revolutionize Yield Management with Ultra-Precise Dispensing (UPD) System

2D, 3D conductive micro interconnections:

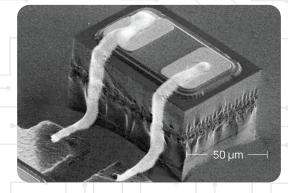
- open defect repair on complex technological substrates and system-on-the-chips
- conductive and insulating microvias in High Density Interconnect (HDI)
- ultra-high resolution features below 1 µm
- photonic sintering enables electrical resistances <1 Ω/μm
- variety of materials: conductive, dielectric, photoresist, QDots and more

State of the art Ag* sub-micron line





3D conductive micro interconnections - MicroLED



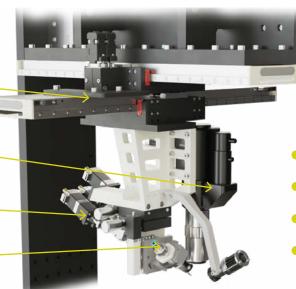
Ultra-precise circuit editing:

- fixing design flaws or intentional changes by local dispensing on 2D/3D connection
- via filling directly on the circuit
- versatile material dispensing on complex technological substrates



UPD System overview

- High-precision XY axis motors
- Ultra-high resolution top-view microscopic optics and camera
- Motorized camera for real-time process _ monitoring
- Cartridge with nozzle in high-precision Z axis



Additional system elements:

- UPD process controller
- Precise pressure control unit
- UPD software
- UPD software API for integration with host system

Why should you choose UPD technology?

1. Patented technology, reliable partner and process for tailored industrial solutions.

1. PoC \longrightarrow 2. Solution tailoring \longrightarrow 3. Production \longrightarrow 4. Integration with host

2. Unprecedented range of feature size XTPL® UPD technology covers:

Ultra-high resolution	High resolution	Medium resolution	Low resolution
0.5 μm	i 1µm	ι 10 μm	50 μm

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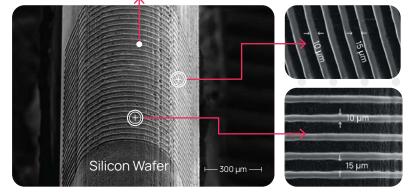
Revolutionize heterogeneous integration with Ultra-Precise Dispensing (UPD) System

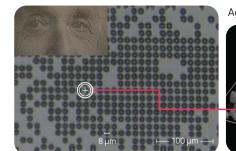
- Ultra-Precise Dispensing
 for integration:
- facilitates seamless integration
- provides flexibility and miniaturization for FHE, IC packaging, IoT systems, antennas, biosensors and more

Precision-Driven Microbumps:

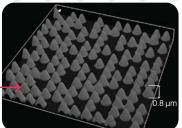
- controlled, precise dispensing of microbumps below 10 μm
- boosts performance in versatile scaling down of the IC packaging, system-on-the-chip designs

Ag* microlines on the edge





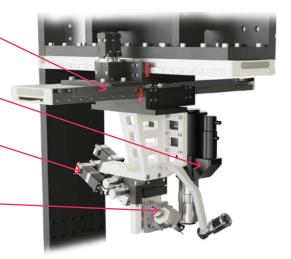
Ag* microbumps



	Reliable Connections via Filling:		Empty via	Via cross sectio	on	• •
	• • • • • • • •					• •
•	enables reliable connections, enhances thermal management and miniaturization		Filled via	532 µm		$ \rightarrow \begin{array}{c} Pt \\ coating \\ \rightarrow \\ Ag^* \end{array} $
••	enhances functionality and performance				26.25 µm	nanopaste
	for advanced packaging, flip-chip and TSV			就是湖南北东	⊢_10 µm—L	
•	variety of materials: conductive, dielectric, photoresist, QDots			• • •	• • • •	
	Doworing Microl ED Integration		MicroLED		Ag* interconnections	
	Powering MicroLED Integration:					
•	facilitates control, driving and power dispensing of microLED displays				6	
•	expands applications in AR/VR,		DA D		19 M	
	smartwatches and automotive displays					50 µm—1

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